ABSTRACT OF THE DISCLOSURE

A communication controller includes a memory circuit and a processor operable in response to data and instructions stored in the memory circuit. A first communication circuit is under control of the processor for communicating between the communication controller and a first remote device according to a first data communication standard. A second communication circuit is under control of the processor for communicating between the communication controller and a second remote device according to a second data communication standard. The second data communication standard is different from the first data communication standard.